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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s)      Katsumi Sameshima      Examiner:      Wai-Sing Louie  
Serial No.:      09/451,979      Group Art Unit:      2814  
Confirmation No.:      Unassigned      Docket:      362-39 RCE  
Filed:      November 30, 1999      Dated:      February 19, 2002  
For:      FERROELECTRIC MEMORY  
            AND METHOD FOR  
            MANUFACTURING SAME

Commissioner for Patents  
Washington, DC 20231

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*20231 on February 19, 2002*

*Dated: 2/19/02*

**REPLY TO FINAL OFFICE ACTION**

Sir:

In response to the final Office Action mailed October 19, 2001, a reply to which is due February 19, 2002 with a Petition for Extension of Time submitted herewith, please amend the following above-identified application as follows:

**IN THE CLAIMS:**

Please amend Claims 1 -5 by rewriting the same as follows:

1. (Twice Amended) 'A ferroelectric memory, comprising:  
an insulation film having a concave portion at a top surface; and  
a laminated body obtained by laminating a plurality of layers on said top surface and  
etching a region of said plurality of layers corresponding to a region other than said concave  
portion, wherein said laminated body includes a lower electrode layer which is brought into  
a portion of said lower electrode layer protrudes outward from an inner peripheral edge

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